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### Compact Wave Logic Quartz Alarm

A wave of the hand interrupts the alarm; Wave your hand at any time to turn on light dial; Unique alarm setting: simply rotate dial ...

[shop.store.yahoo.com/airline/comwavlogqua.html](http://shop.store.yahoo.com/airline/comwavlogqua.html) - 5k - [Cached](#) - [Similar pages](#)

[onerror {resume} add wave -logic /super\\_bench/clock add wave -logic ...](#)

[onerror {resume} add wave -logic /super\\_bench/clock add wave -logic /super\\_bench/reset\\_n](#)

[add wave -literal -hexadecimal /super\\_bench/addr\\_mem add wave -literal ...](#)

[www.idt.mdh.se/kurser/ct3170/labbar/lab5/release/do/wave.do](http://www.idt.mdh.se/kurser/ct3170/labbar/lab5/release/do/wave.do) - 2k - [Cached](#) - [Similar pages](#)

### [PDF] Chemical Wave Logic Gates

File Format: PDF/Adobe Acrobat - [View as HTML](#)

Chemical Wave Logic Gates Oliver Steinbock, † Petteri Kettunen, ‡ and Kenneth Showalter\* Department of Chemistry, West Virginia UniVersity, Morgantown ...

[www.chem.fsu.edu/editors/STEINBOCK/papers/jpc96a.pdf](http://www.chem.fsu.edu/editors/STEINBOCK/papers/jpc96a.pdf) - [Similar pages](#)

### BlitzCoder Worklogs

BlitzCoder Worklogs, Main Worklogs Page. "Wave logic". Owner: morduun.

Showcase Entry: Vapem! Total Entries: 4. morduun Entry: 4 11/16 ...

[www.blitzcoder.com/cgi-bin/worklogs/plan...showentry.pl?id=morduun10022002012810&comments=no](http://www.blitzcoder.com/cgi-bin/worklogs/plan...showentry.pl?id=morduun10022002012810&comments=no) - 15k -

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### [PPT] Asynchronous Wave-Pipelines

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... General Relations. 8. Synchronous Wave Pipeline. Wave Logic. Latch/Reg. Latch/Reg.

Data. ... Clock. 10. Asynchronous Wave Pipeline (AWP). Wave Logic. Wave Latch. Wave Latch.

Data. ...

[www.ee.technion.ac.il/vlsi/async2000/presentation/OliverHauck.ppt](http://www.ee.technion.ac.il/vlsi/async2000/presentation/OliverHauck.ppt) - [Similar pages](#)

### ECE 311 - ModelSim Tutorial

... Listing 1: Sample Do File # Clear previous forces and wave window restart -force

-nowave # Insert signals in wave display window add wave -logic slowclk add ...

[courses.ece.uiuc.edu/ece311/docs/modelsim/tutorial.html](http://courses.ece.uiuc.edu/ece311/docs/modelsim/tutorial.html) - 4k - [Cached](#) - [Similar pages](#)

### Showalter Research Group

Oliver Steinbock, Petteri Kettunen, and Kenneth Showalter Chemical

Wave Logic Gates. J. Phys. Chem., 100, 18970-18975 (1996). Logic ...

[heracles.chem.wvu.edu/new/abs.asp?a=2](http://heracles.chem.wvu.edu/new/abs.asp?a=2) - 7k - [Cached](#) - [Similar pages](#)

### [PPT] Asynchronous Wave-Pipelines

File Format: Microsoft Powerpoint 97 - [View as HTML](#)

... completion detection. 9. Synchronous Wave Pipeline. Wave Logic. Latch/Reg. Latch/Reg.

Data. ... 12. Asynchronous Wave Pipeline (AWP). Wave Logic. Wave Latch. Wave Latch. Data. ...

[www.vlsi.informatik.tu-darmstadt.de/publications/downloads/hauck00a.ppt](http://www.vlsi.informatik.tu-darmstadt.de/publications/downloads/hauck00a.ppt) - [Similar pages](#)

[subroutine waveit \(u, logic, error\) use cesrv\\_struct use ...](#)

[... if \(u%wave%wave\\_what == phase\\_data\\$\) then if \(u%wave%plane == x\\_plane\\$\) then call wave\\_phase\(u%phase%x, u%tune%x, u%wave, logic\) else call wave\\_phase\(u%phase%y ...](#)

[www.ins.cornell.edu/~dcs/cesrv/waveit.f90](http://www.ins.cornell.edu/~dcs/cesrv/waveit.f90) - 2k - [Cached](#) - [Similar pages](#)

### World Science Forum, Budapest

... computer combines spatial temporal waves and logic (locally and globally); hence

it is also called wave-computer and the associated logic as wave-logic. ...

[www.sciforum.hu/index.php?image=speakers&content=spc\\_roska](http://www.sciforum.hu/index.php?image=speakers&content=spc_roska) - 17k - Aug 3, 2004 - [Cached](#) - [Similar pages](#)

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**1 An introductory digital design course using a low-cost autonomous robot**

Newman, K.E.; Hamblen, J.O.; Hall, T.S.;

Education, IEEE Transactions on , Volume: 45 , Issue: 3 , Aug. 2002

Pages:289 - 296

[\[Abstract\]](#) [\[PDF Full-Text \(308KB\)\]](#) IEEE JNL**2 Rapid prototyping using field-programmable logic devices**

Hamblen, J.O.;

Micro, IEEE , Volume: 20 , Issue: 3 , May-June 2000

Pages:29 - 37

[\[Abstract\]](#) [\[PDF Full-Text \(1536KB\)\]](#) IEEE JNL**3 An undergraduate computer engineering rapid systems prototyping design laboratory**

Hamblen, J.O.; Owen, H.L.; Yalamanchili, S.; Binh Dao;

Education, IEEE Transactions on , Volume: 42 , Issue: 1 , Feb. 1999

Pages:8 - 14

[\[Abstract\]](#) [\[PDF Full-Text \(112KB\)\]](#) IEEE JNL**4 A VHDL synthesis model of the MIPS processor for use in computer architecture laboratories**

Hamblen, J.O.;

Education, IEEE Transactions on , Volume: 40 , Issue: 4 , Nov. 1997

Pages:10 pp.

[\[Abstract\]](#) [\[PDF Full-Text \(16KB\)\]](#) IEEE JNL**5 The design of a neural network with a biologically motivated architecture**

Braham, R.; Hamblen, J.O.;

Neural Networks, IEEE Transactions on , Volume: 1 , Issue: 3 , Sept. 1990

Pages:251 - 262

[\[Abstract\]](#) [\[PDF Full-Text \(1220KB\)\]](#) IEEE JNL**6 An instructional laboratory to support microprogramming**

Hamblen, J.O.; Parker, A.; Rohling, G.A.;

Education, IEEE Transactions on , Volume: 33 , Issue: 4 , Nov. 1990

Pages:333 - 336